

FIG. 1

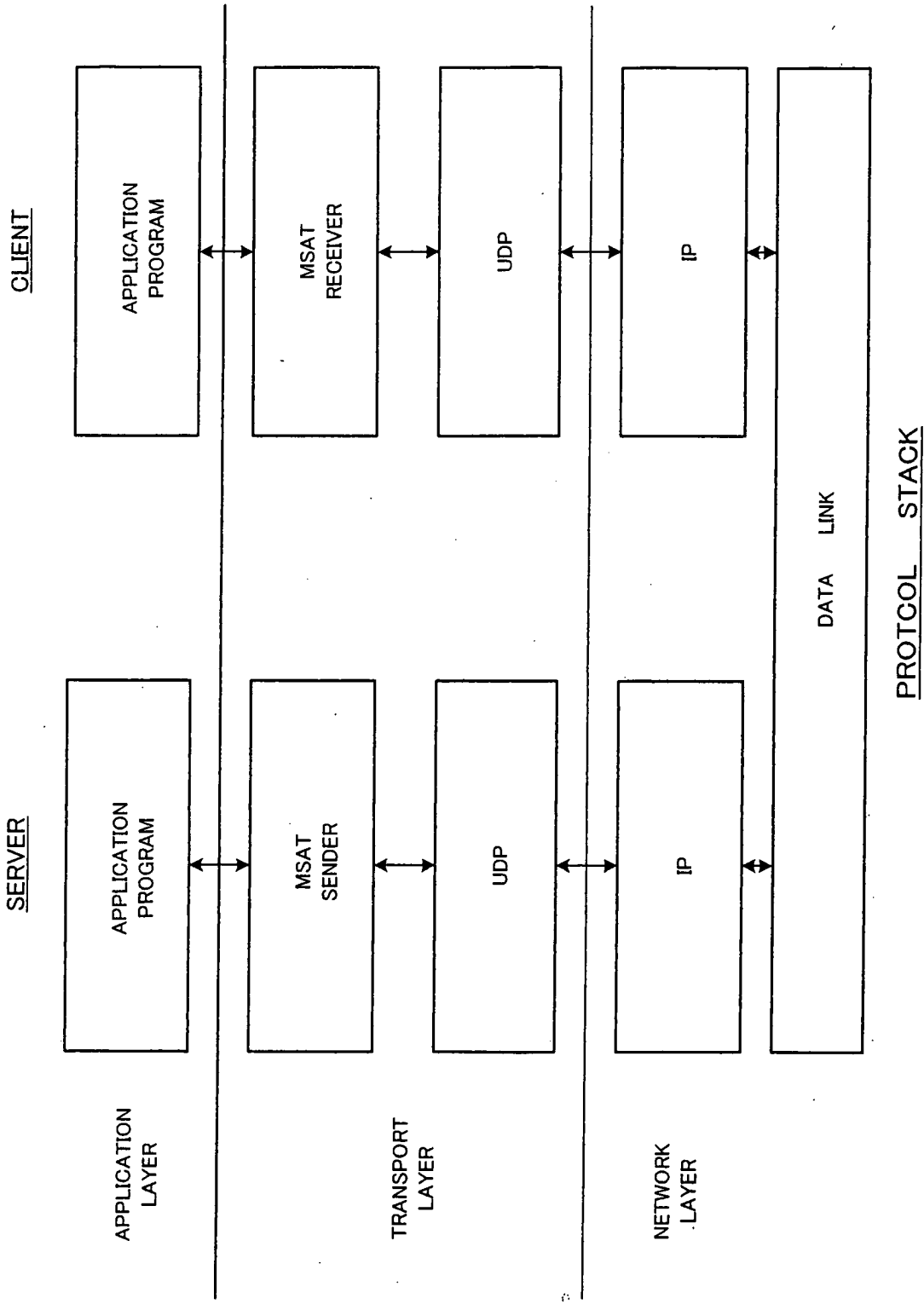
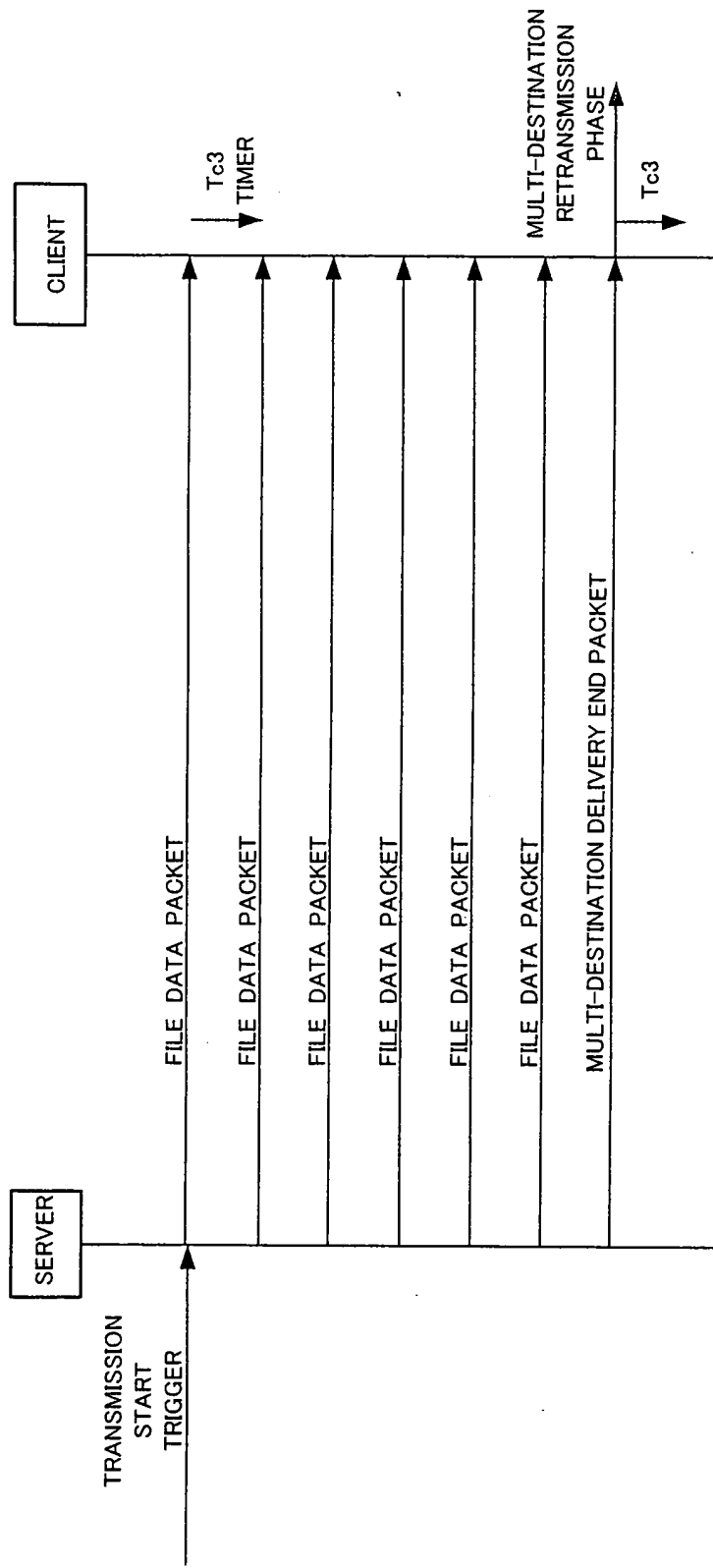
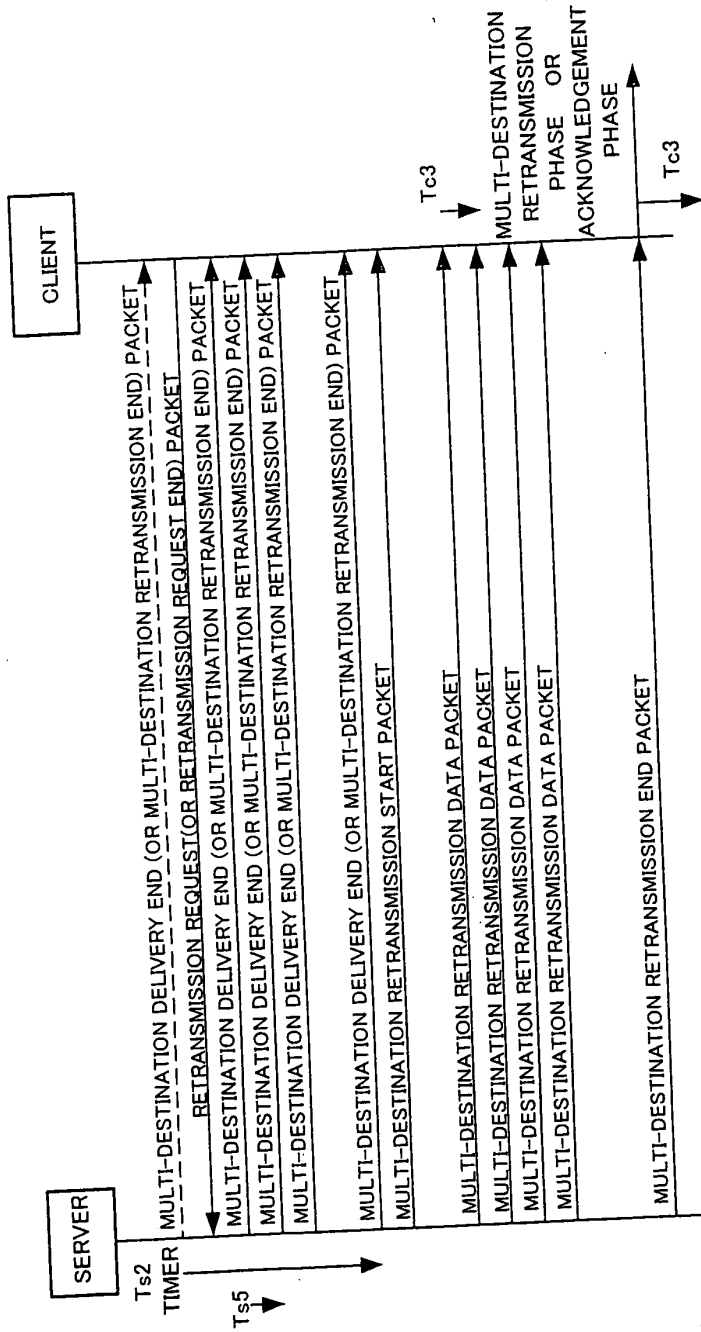


FIG.3



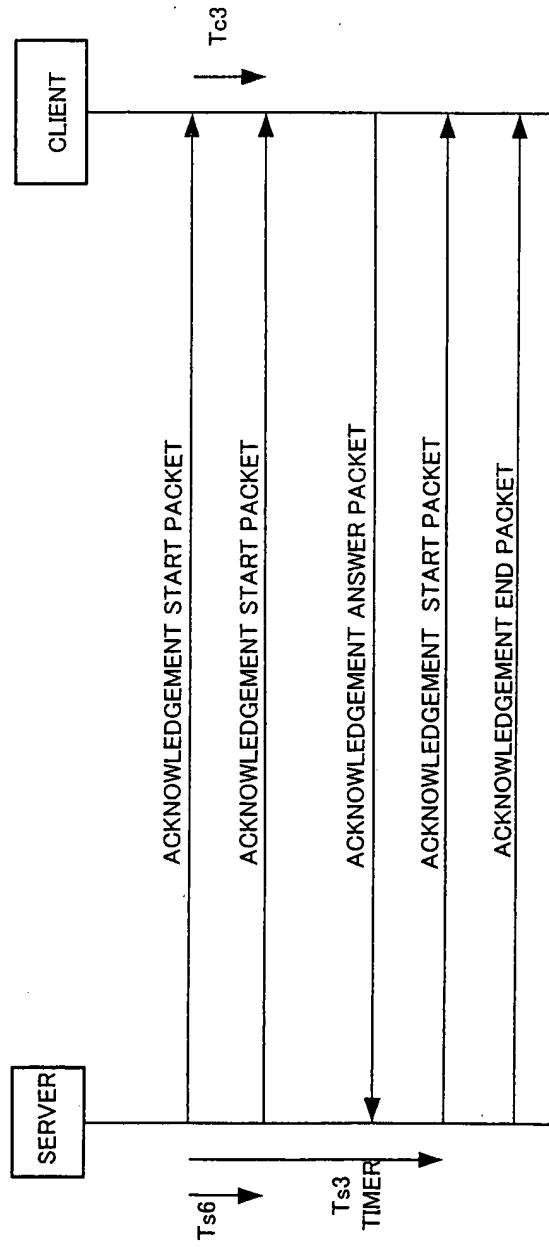
SEQUENCE OF MULTI-DESTINATION DELIVERY PHASE

FIG. 4



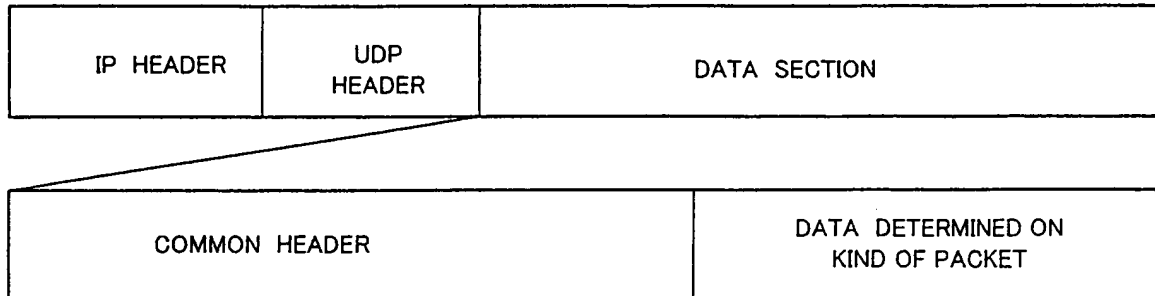
SEQUENCE OF MULTI-DESTINATION RETRANSMISSION PHASE

FIG.5



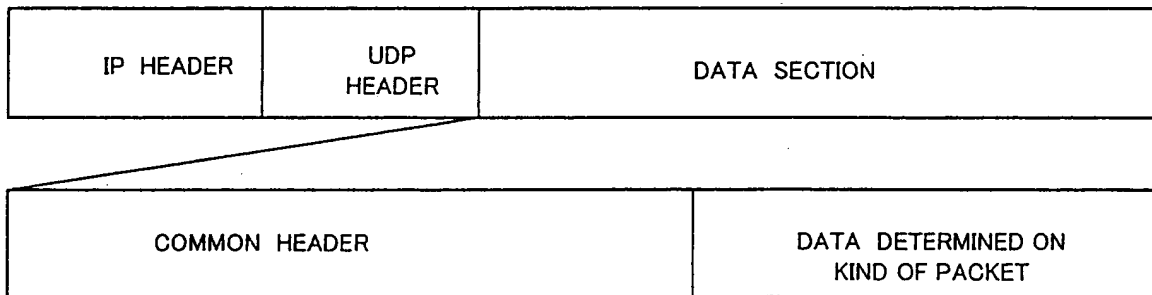
SEQUENCE OF ACKNOWLEDGEMENT PHASE

FIG.6



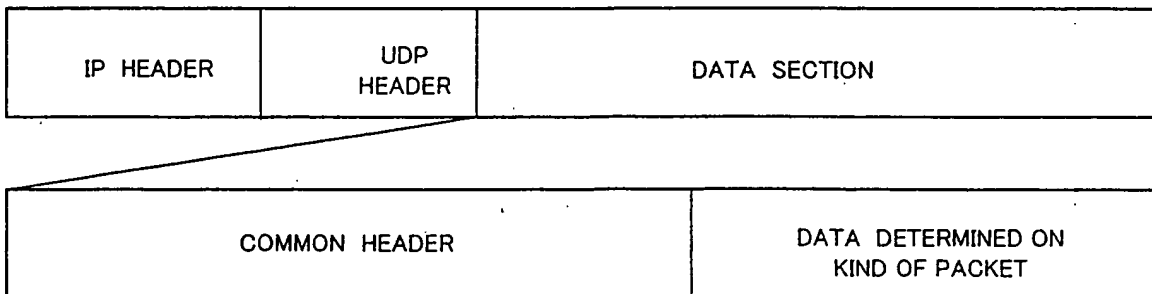
BASIC PACKET FORMAT OF ANNOUNCE REGISTRATION PHASE

FIG.7



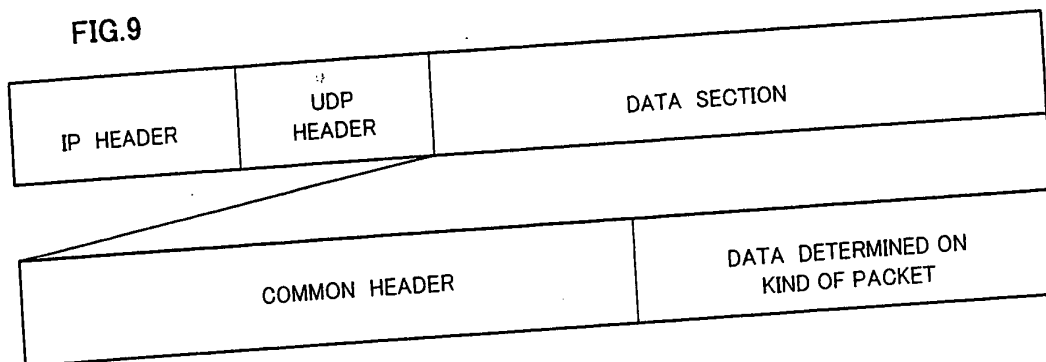
BASIC PACKET FORMAT OF MULTI-DESTINATION
DELIVERY PHASE

FIG.8



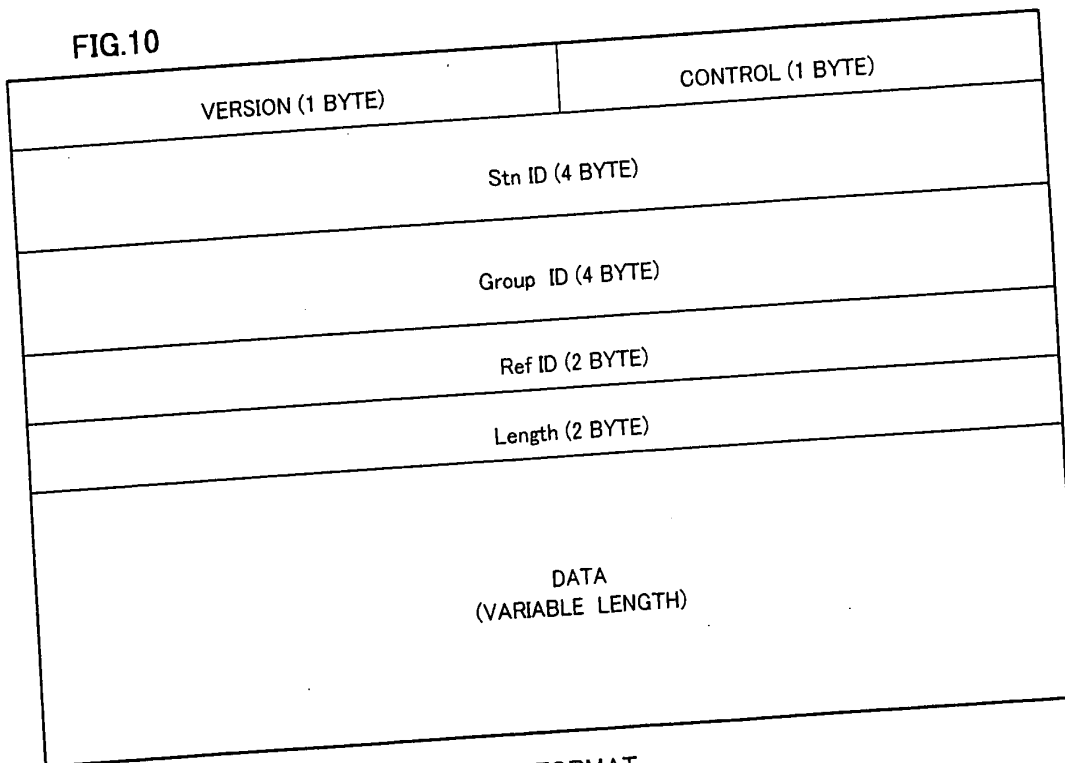
BASIC PACKET FORMAT OF MULTI-DESTINATION
RETRANSMISSION PHASE

FIG.9



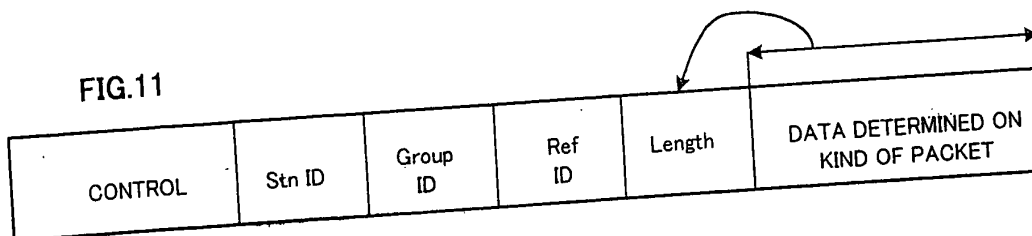
BASIC PACKET FORMAT OF ACKNOWLEDGEMENT PHASE

FIG.10



HEADER FORMAT

FIG.11



DEFINITION OF CODE LENGTH

09928137.081001

[illegible]

090637Z JUL 80

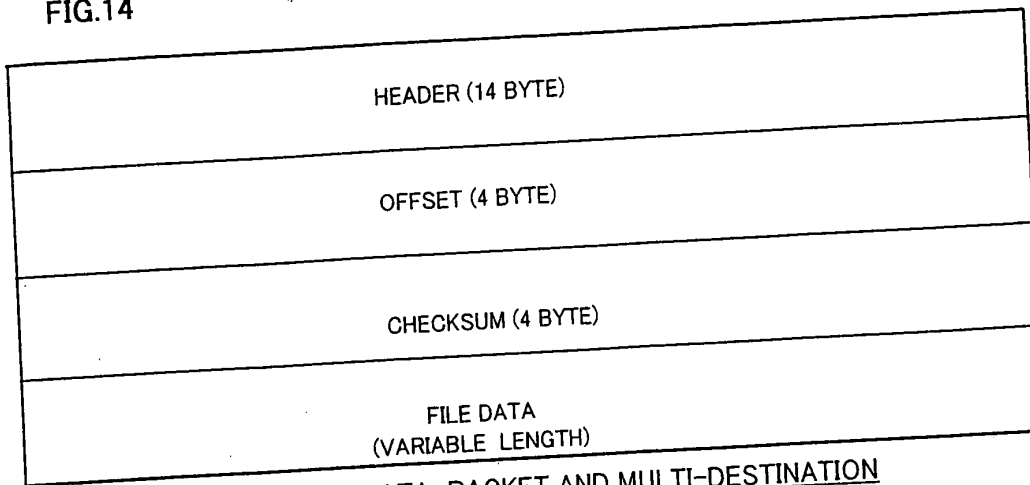
090637Z JUL 80

090637Z JUL 80

090637Z JUL 80

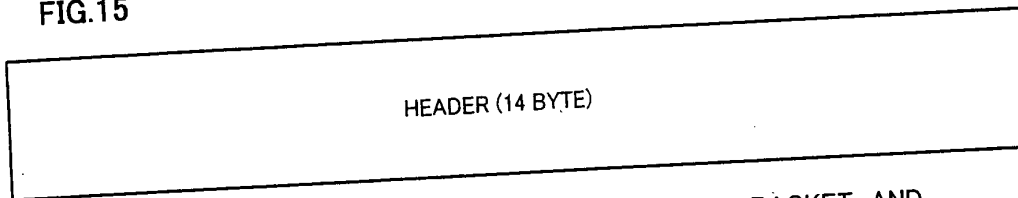
090637Z JUL 80

FIG.14



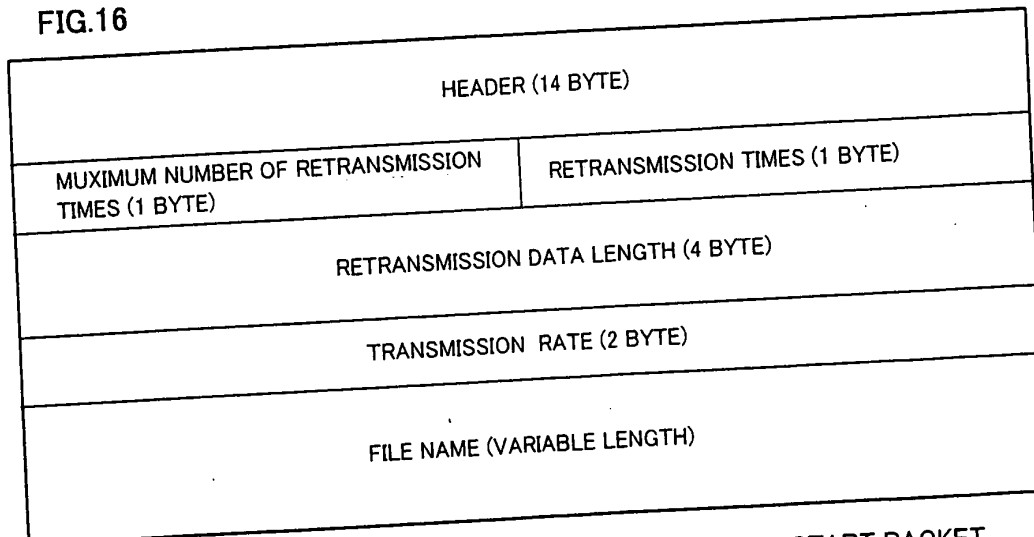
FORMAT OF FILE DATA PACKET AND MULTI-DESTINATION
RETRANSMISSION DATA PACKET

FIG.15



FORMATS OF MULTI-DESTINATION DELIVERY END PACKET AND
MULTI-DESTINATION RETRANSMISSION END PACKET

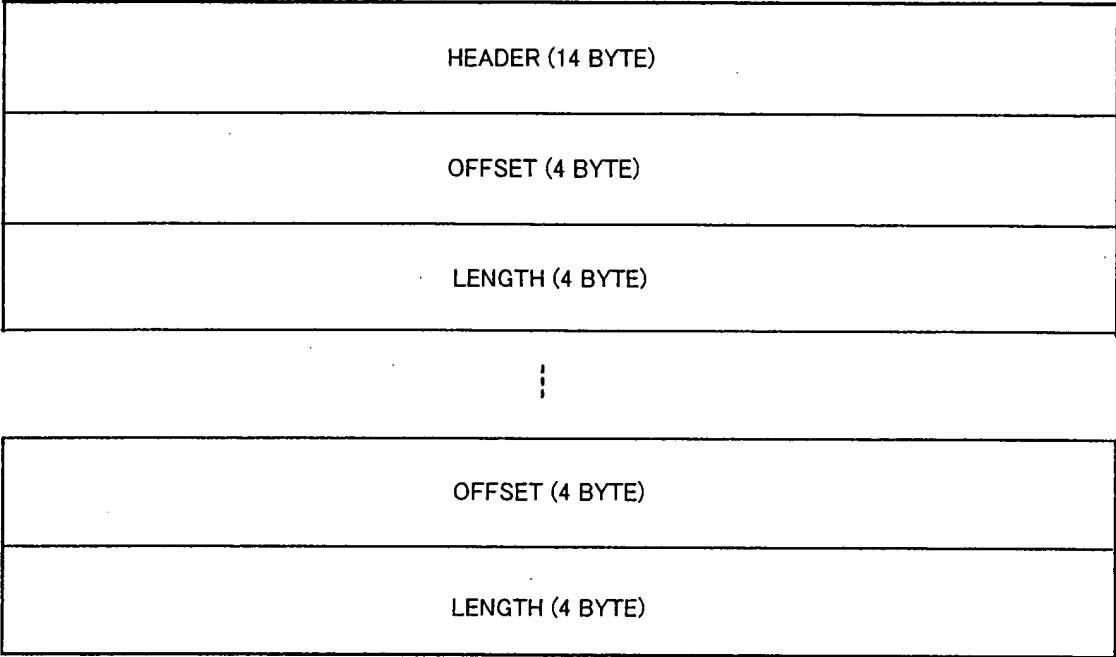
FIG.16



FORMATS OF MULTI-DESTINATION RETRANSMISSION START PACKET
AND MULTI-DESTINATION RETRANSMISSION END PACKET

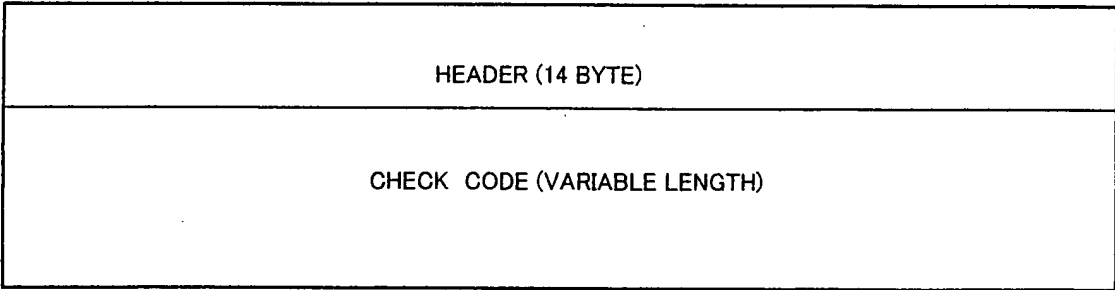
092613-081001
FOOTST-2E32660

FIG.17



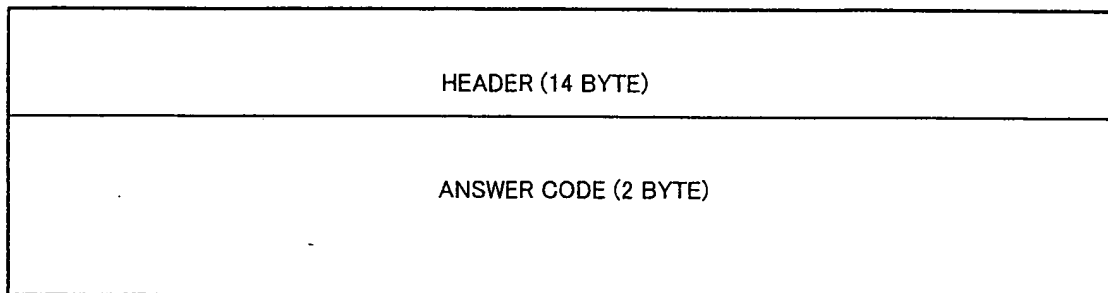
FORMATS OF
RETRANSMISSION REQUEST PACKET AND
RETRANSMISSION REQUEST END PACKET

FIG.18



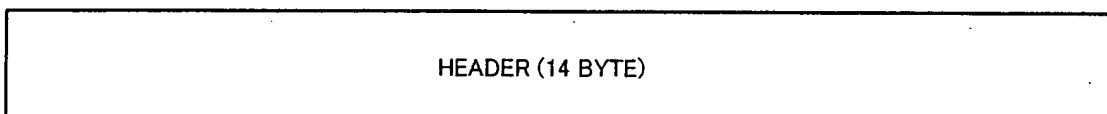
FORMAT OF ACKNOWLEDGEMENT START PACKET

096513-09301



FORMAT OF ACKNOWLEDGEMENT
ANSWER PACKET

FIG.20



FORMAT OF ACKNOWLEDGEMENT
END PACKET

FIG.21A

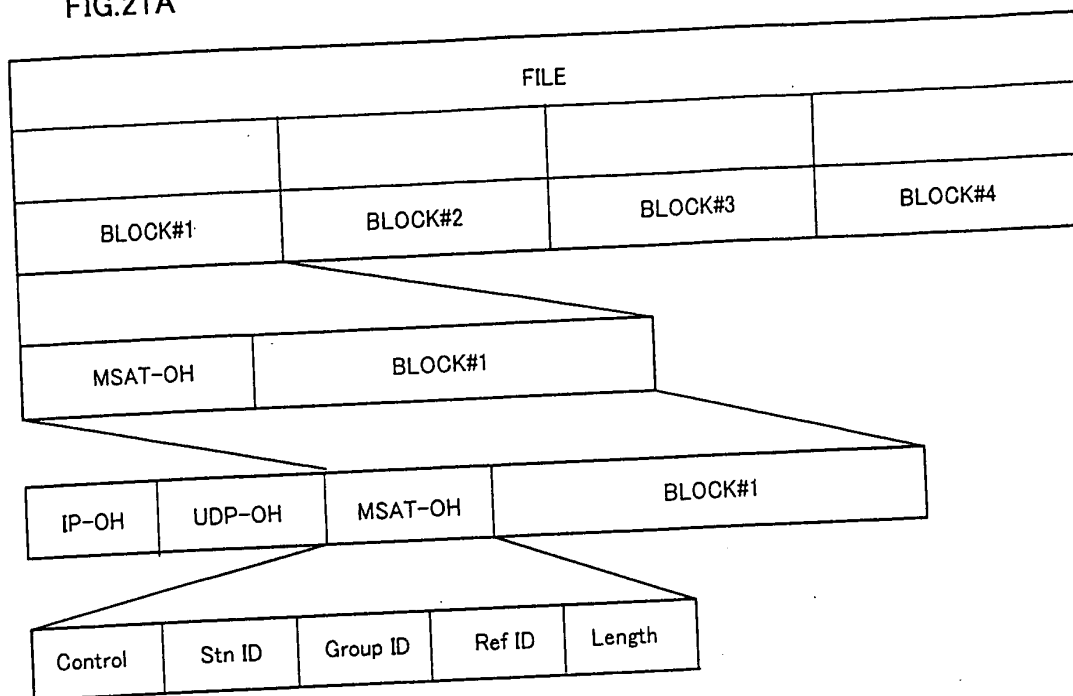


FIG.21B

SEQUENCE NO.	DATA LENGTH	SEQUENCE NO.	DATA LENGTH
--------------	-------------	--------------	-------------

09928137-081001

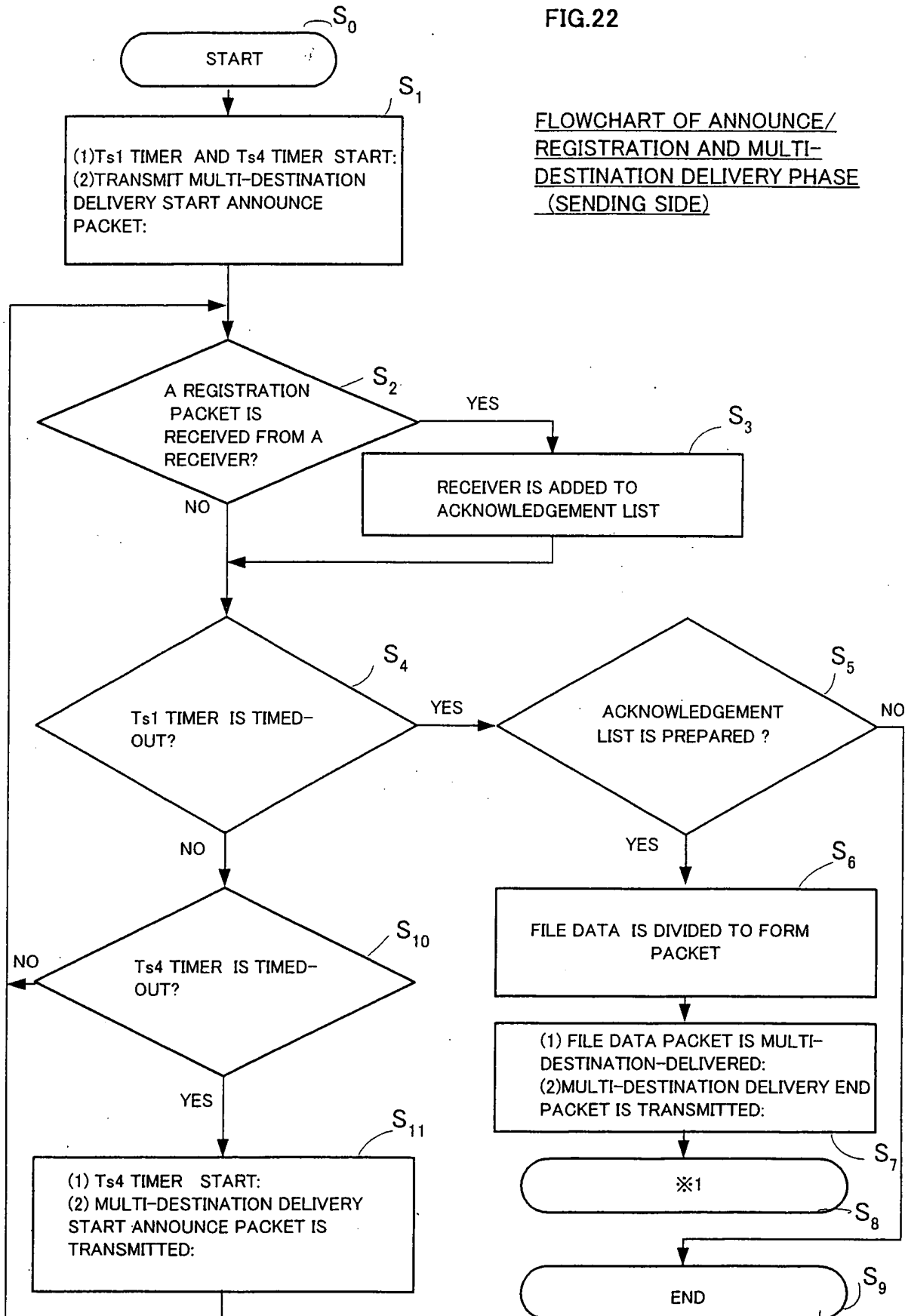


FIG.23

FLOWCHART OF ANNOUNCE/
REGISTRATION AND MULTI-
DESTINATION DELIVERY PHASE
(RECEIVING SIDE)

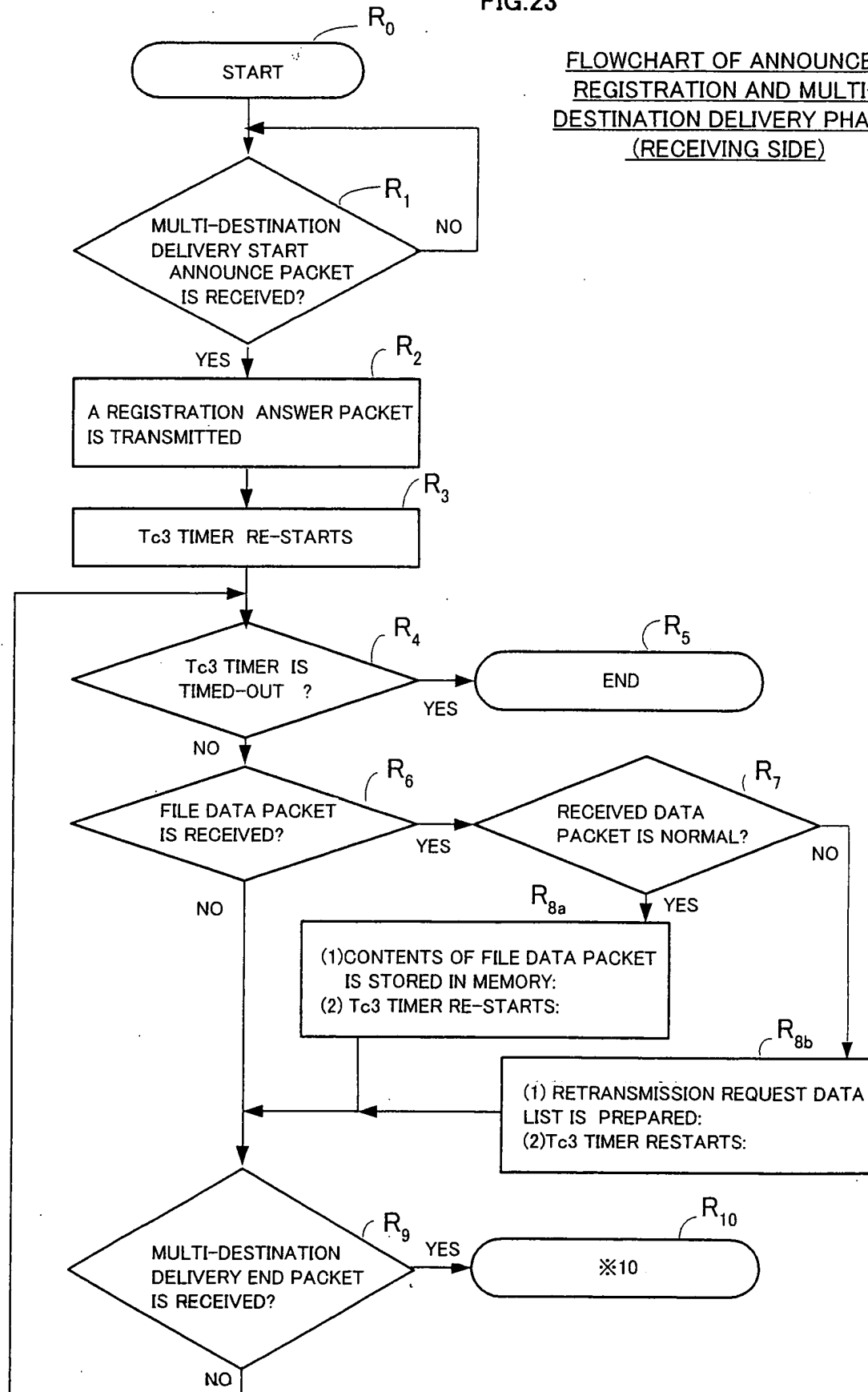
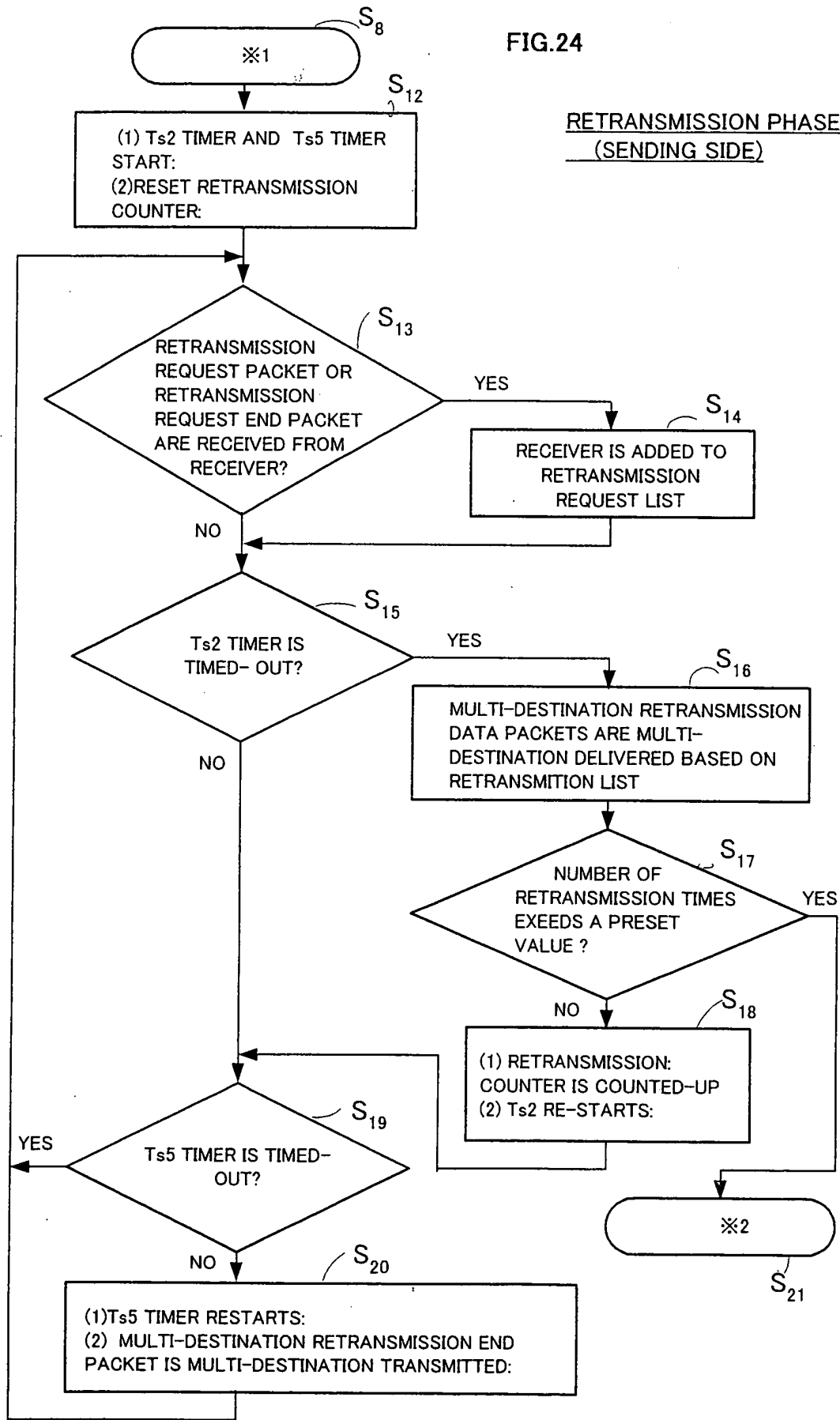


FIG.24

RETRANSMISSION PHASE
(SENDING SIDE)



09928137-031004

FIG.25 RETRANSMISSION
PHASE
(RECEIVING SIDE)

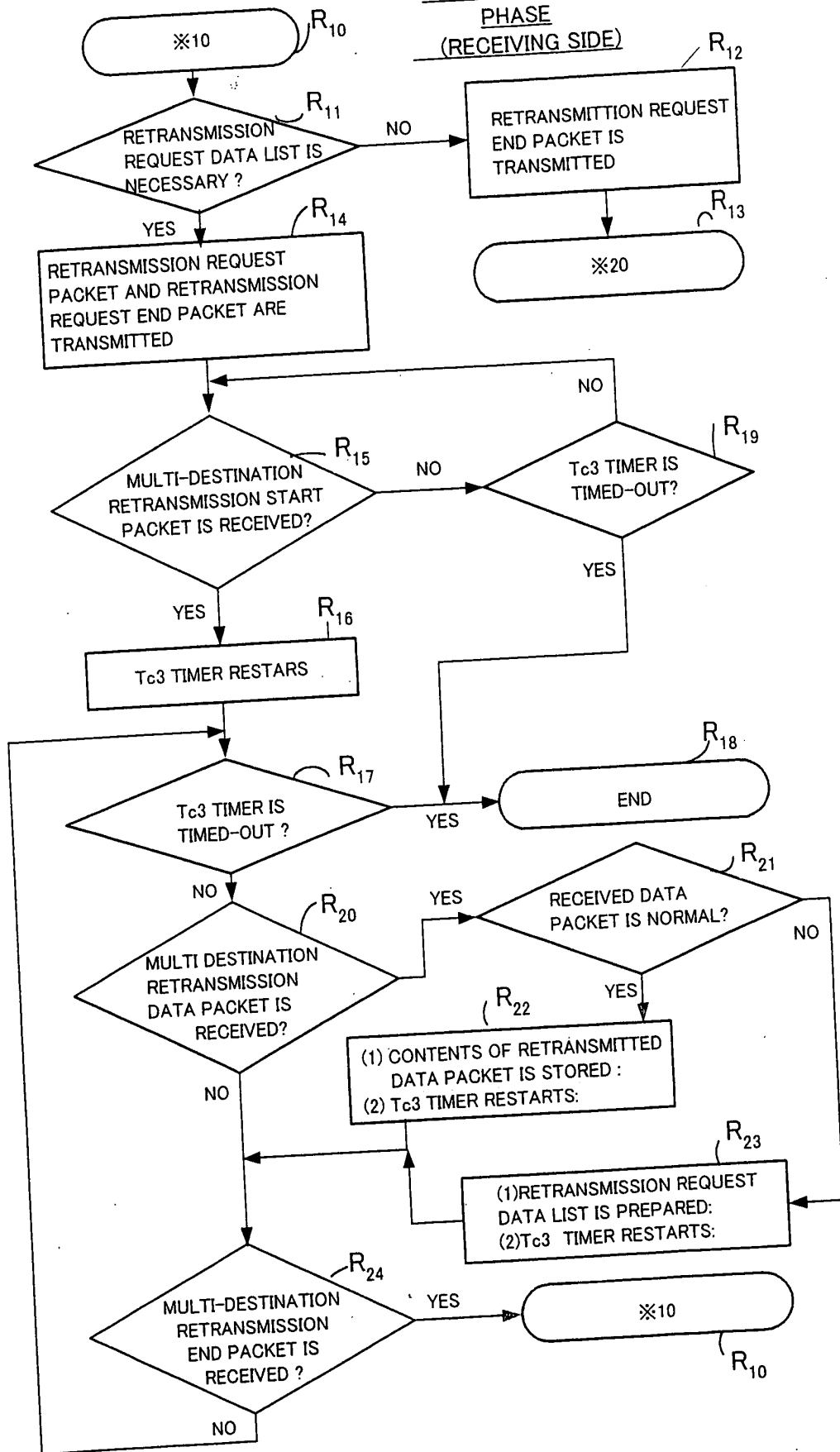


FIG. 25

ACKNOWLEDGEMENT PHASE
(SENDING SIDE)

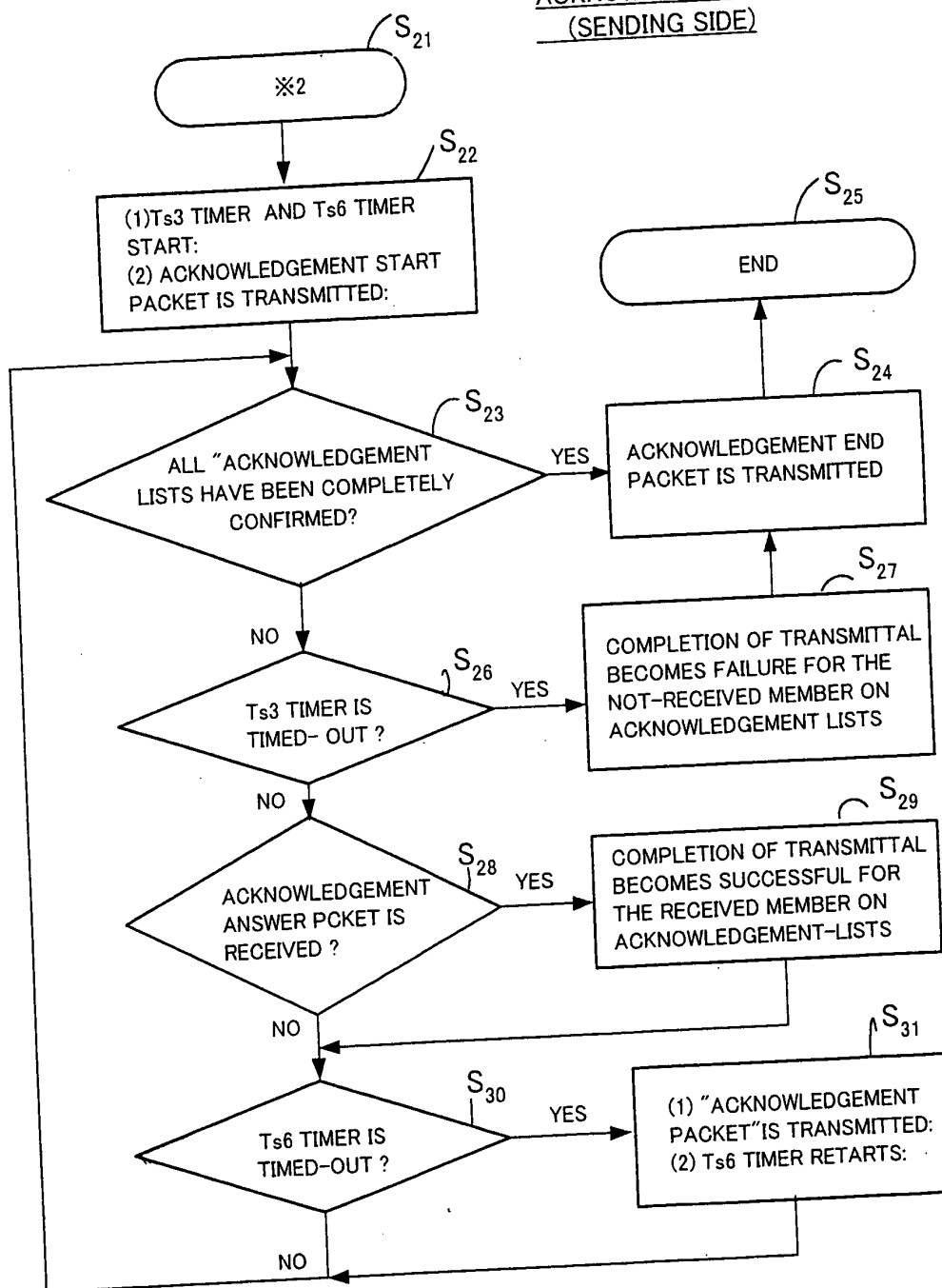
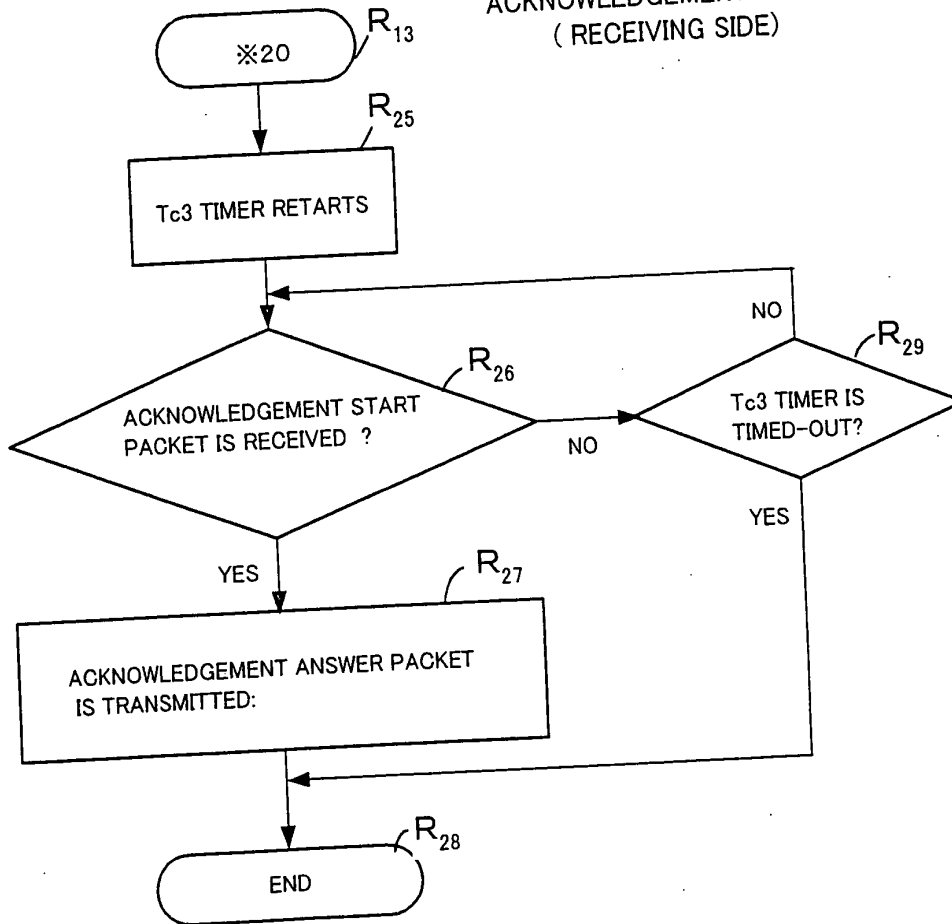


FIG.27

ACKNOWLEDGEMENT PHASE
(RECEIVING SIDE)



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FIG.28A

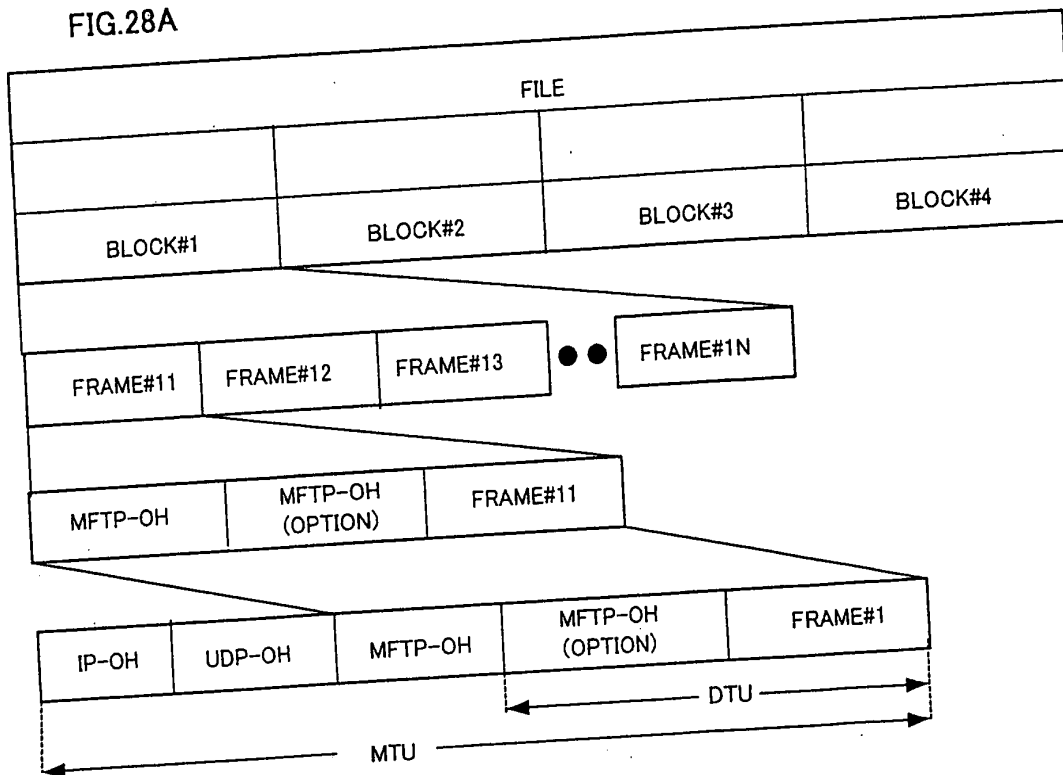


FIG.28B

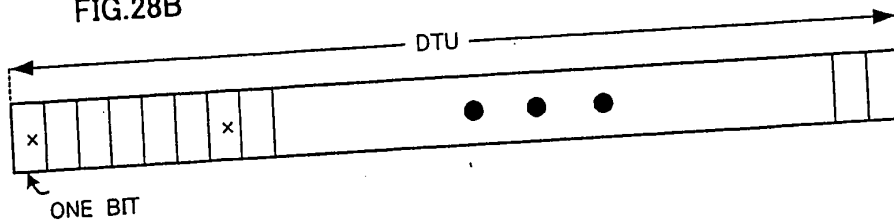


FIG.29

CIRCUIT BIT ERROR RATE

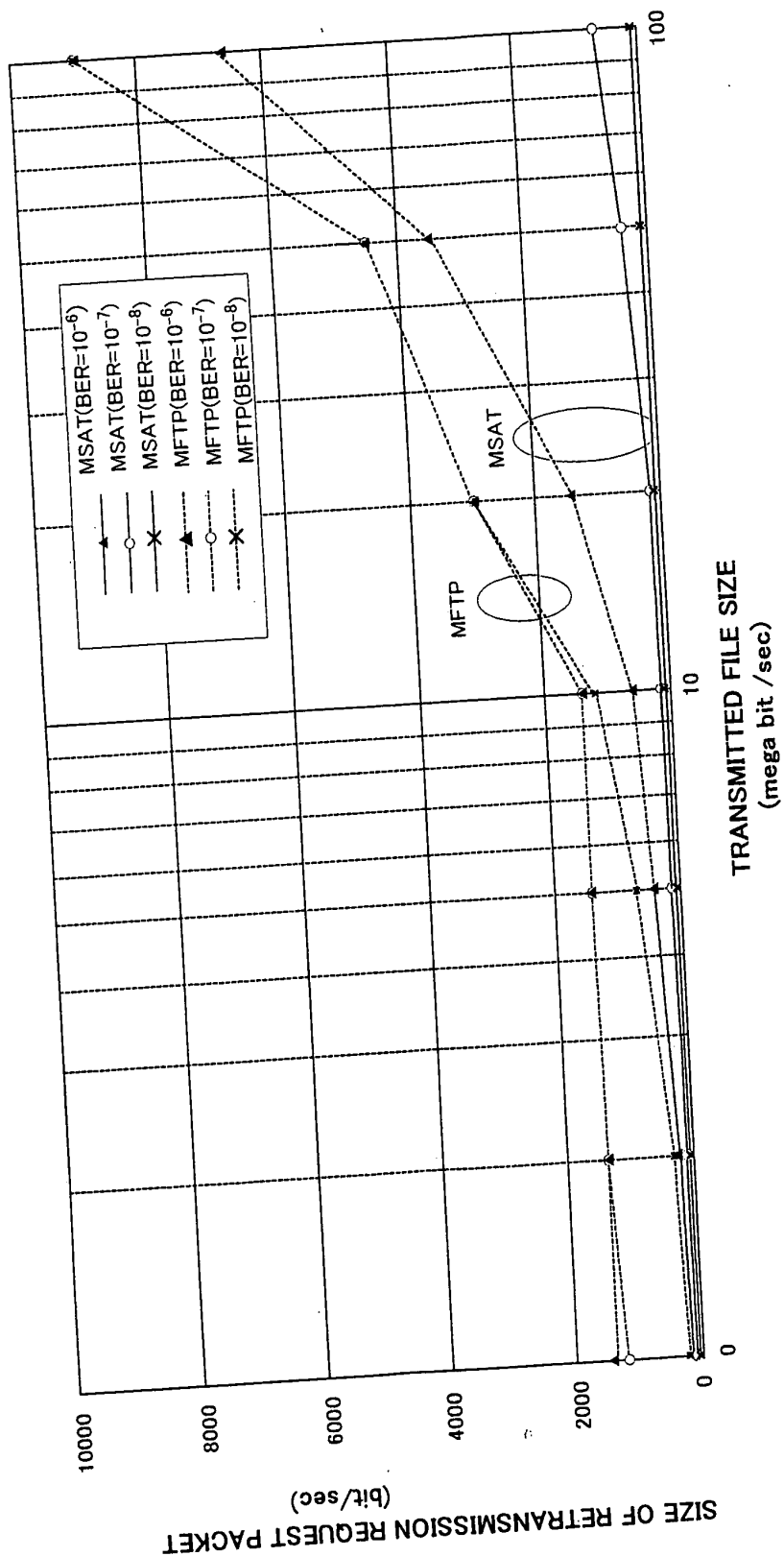


FIG.30

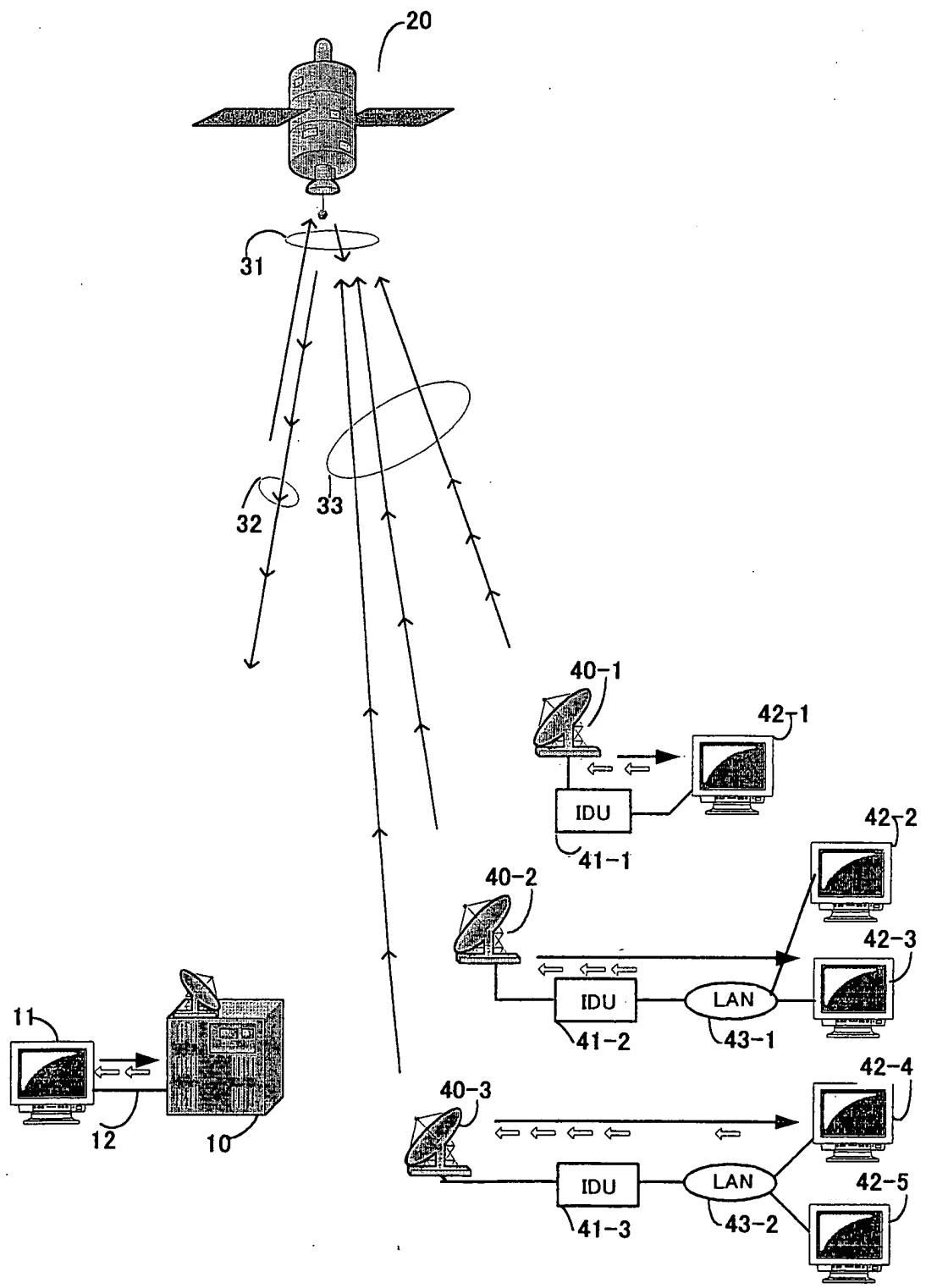


FIG. 30